PATENT

Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Louis L. HSU, et al.

GROUP ART UNIT: 2814

APPLN NO:

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EXAMINER:

Rao, Shrinivas

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June 13, 2001

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January 23, 2004

FOR:

T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR AND A PSEUDO-TFT TRANSFER GATE AND A METHOD

FOR FABRICATING THE SAME

SUPPLEMENTAL AMENDMENT

Sir:

In accordance with the telephone interviews held between the Examiner and for Applicants' representative Jeff Kirshner on January 21, 2004 and January 22, 2004, Applicants propose the following remarks and amendments in order to place the claims in condition for allowance.

This amendment is being provided as a supplement to the Amendment filed on August 7, 2003 response to the Office Action dated May 7, 2003.

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office (703) 746-7239 on January 23, 2004.

Dated: January 23, 2004

Janette O. Zut